\*\* The EEPROM array is organized as rows of word (2 bytes), the EEPROM block's

\*\* erase sector size is 2 rows (2 words). Therefore it is preferable

\*\* to use word aligned data for writting - methods SetWord and SetLong -

\*\* with word aligned address.

\*\* EEPROM size : 4096 byte

\*\* Initialization:

\*\* Wait in methods : Enabled

\*\* EEPROM clock : 181 kHz

\*\*

\*\* Contents :

\*\* SetByte - byte IEE1\_SetByte(IEE1\_TAddress Addr, byte Data);

\*\* GetByte - byte IEE1\_GetByte(IEE1\_TAddress Addr, byte \*Data);

\*\* SetWord - byte IEE1\_SetWord(IEE1\_TAddress Addr, word Data);

\*\* GetWord - byte IEE1\_GetWord(IEE1\_TAddress Addr, word \*Data);

\*\* SetLong - byte IEE1\_SetLong(IEE1\_TAddress Addr, dword Data);

\*\* GetLong - byte IEE1\_GetLong(IEE1\_TAddress Addr, dword \*Data);

typedef unsigned int word

#ifndef \_\_BWUserType\_IEE1\_TAddress

#define \_\_BWUserType\_IEE1\_TAddress

/\* User type for addressing of the EEPROM. Actual type depends on the CPU family. \*/

typedef far word \* far IEE1\_TAddress; /\* Type of address to the EEPROM \*/

#endif

/\* EEPROM area start address (in the format used by bean methods). \*/

#define IEE1\_AREA\_START ((IEE1\_TAddress)1306624UL) 0XF0000 – 13FFFB

/\* EEPROM area last address (in the format used by bean methods). \*/

#define IEE1\_AREA\_END ((IEE1\_TAddress)1310715UL)

-Start Address **0x13\_F000** End Address **0x13\_FFFF PG-42**

13fffb-13f000